

AMENDMENT TO THE SPECIFICATION

Please replace the Title of the Invention as follows:

METHODS FOR RENAMING A MEMORY REFERENCE TO STACK LOCATIONS

REFERENCES TO PROCESSOR REGISTERS IN A COMPUTER PROCESSING SYSTEM

Please replace the paragraph starting on page 9, line 1, as follows:

U.S. Patent No. 5,911,057, entitled "Superscalar Microprocessor Having Combined Register and Memory Renaming Circuits, Systems, and Methods", issued on June 8, 1999, the disclosure of which is incorporated herein by reference, describes an architecture for renaming memory and register operands and in uniform fashion. Memory coherence is based on "snooping" memory requests. While this approach is sufficient for in the in-order execution of memory operations in a multiprocessor computing system, out-of-order operation in a multiprocessor system may generate incorrect results. U.S. Patent No. 5,838,941, entitled "Out-of-order Superscalar Microprocessor with a renaming Device that Maps Instructions from memory to Registers", issued on November 17, 1998, the disclosure of which is incorporated herein by reference, describes symbolic renaming of memory references. The invention deals with equivalence of all types, and requires lookup lookup of an associative array to establish equivalence between expression and names. This results in a complex architecture with potentially severe cycle time impact.